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RESEARCH IN MICROTERMINAL DEVELOPMENT
AND NETWORK END-TO-END ERROR RECOVERY

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California University

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13. ABSTRACT In Task I (Microterminal), we have pursued the implementation of a prototype terminal which will allow the direct replacement of new and more capable components as they become available. All prototype parts have been ordered except the housing and delivery of all parts should be complete by mid-April. A breadboard version is underway and the prototype is scheduled for completion before June. Task II (End-to-End) work for this quarter has been absorbed by implementing the hardware for the processor pair which will be responsible for communication and error recovery. One processor is ahead of the other in fabrication and will be used for software development as soon as it is completed. Design of the first transmission program is already underway using another Network site.		

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14. KEY WORDS	LINK A		LINK B		LINK C	
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SECOND QUARTERLY TECHNICAL REPORT
RESEARCH IN MICROTERMINAL DEVELOPMENT
AND NETWORK END-TO-END ERROR RECOVERY

I. SCOPE OF QUARTERLY TECHNICAL REPORT

Three separate subject areas make up the body of this quarterly report. These may be treated as the latest in a series of reports dealing with Task J of our project: Microterminal Research and Development. Included are:

1. Report #6 - Summary of Display Considerations, both past and through 30 March 1975.
2. Report #7 - Portable Intelligent Terminal Command Summary.
3. Report #8 - Prototype Power Sources, Specification and Diagrams.

Our Management Report for this same quarter conveys additional information as to Task II (End-to-End Error Recovery), project schedule, accomplishments, and major concerns.

II. REFERENCES

Previous reports which are supportive of material given in Section III include:

1. First Quarter Technical Report, Report #1, 12/9/74.
2. Prototype Terminal Display Selection, Report #2, 12/20/74.
3. Display for Prototype Vendor Summary, Report #3, 2/4/75.
4. Prototype Power Source, Report #4, 1/17/75.
5. Controller for Microterminal Prototype, Report #5, 2/26/75.
6. First Quarter Management Report, CSL-49, 2/28/75.
7. Second Quarter Management Report, CSL-51, 4/22/75.

III. REPORTS - Quarter ending 31 March 1975

SUMMARY OF DISPLAY CONSIDERATIONS

OVERVIEW

An ongoing survey of display technologies applicable to the Micro-Terminal was started in the Fall 1974. The purpose of the survey being twofold:

1. Identify a company with a capability to supply a display within the time frame and cost constraint for the engineering model.
2. Monitor emerging display technologies which promised performance and/or cost advantages for future application.

The Micro-Terminal requires a multi-line display with a minimum of 40 characters per line. The 40 character line must not exceed 8 inches in length which dictates a maximum horizontal character pitch of 0.2 inches. The required vertical pitch is satisfied with the commonly used character aspect ratios based on the previously stated horizontal pitch.

Preliminary results indicated that a suitable display was not readily available as a standard product and must therefore be acquired on a custom basis. The preliminary results further suggested the following technologies as the most likely candidates:

1. Liquid crystals
2. Light emitting diodes
3. Gas discharge
4. Electroluminescent
5. Cathode ray tubes

LIQUID CRYSTAL DISPLAYS

The liquid crystal development effort encompasses varied approaches. The accompanying chart depicts the principal approaches with the associated company. Included in the chart are salient comments on each approach and the reason for rejection for incorporation in the engineering model. (See Appendix A chart for status of development as of the Fall 1974.)

LIQUID CRYSTAL DISPLAY TECHNOLOGY
 Types: Dynamic Scattering & Field Effect
 Modes: Reflective & Transmissive

Implementation Approaches

(1)

Direct Addressed
w/o Intrinsic Memory

(2)

External Switch and
Memory Element per
Segment

(3)

LSI Semiconductor
Techniques/L.C. Electro
Optical Elements

(4)

Thin Film Semiconductor
Techniques/L.C. Electro
Optical Elements

Princeton Material Sciences
Hughes

Princeton Material Sciences
Itek

Westinghouse

Advantages:

1. Simple Process.
2. Min. Associated Circuitry.
3. Potential for Low Cost.

Advantages:

1. Requirement for exacting material properties reduced significantly from (1).

Advantages:

1. High character density.
2. L.C. Material requirement same as (2).

Advantages:

1. Potential for high character density
2. L.C. Material requirement same as (2).

Disadvantages:

1. Requirement for exacting L.C. material properties.
2. Slow response time.
3. Requires Refresh.
4. Limited multiplexing capability

Disadvantages:

1. Large number of high density interconnections.
2. Low character density.

Disadvantages:

1. Complex batch process.

Disadvantages:

1. Complex batch process.

PMS - Delivery
Hughes - Cost

PMS - Character Density
Itek - Character Density

Cost & Delivery

CURRENT STATUS OF LIQUID CRYSTAL DISPLAY DEVELOPMENT

Direct Addressed Type

Princeton Material Sciences--Indeterminate

Hughes--Development work in this area is being continued with the following significant results:

1. Multiplexing capability extended from 7 lines to 14 lines
2. Verbal expression of confidence in supplying a 2 line 40 character line module with an overall length of less than 5 inches and stackable in the vertical dimension.

External Switch/Memory Element Per Segment

Princeton Material Sciences--Indeterminate

Itek--The Itek effort is aimed at a particular display requirement apparently in the military area. Itek has considered extending the low end of the operational temperature range by incorporating heaters. Low power electroluminescent light sources have been considered for use in low intensity ambient light.

Itek personnel feel that they can now achieve a horizontal character pitch of 0.2 inches which satisfies our minimum requirement. The Itek approach is a solid one but affords little hope of a significant advance in character density.

LSI-Semiconductor Techniques/L.C. Electro-Optical Elements

Hughes--The work has been extended to include silicon on sapphire technology which affords the following advantages over the bulk silicon approach:

1. Increased element density
2. Reduced process complexity
3. Reduced light sensitivity
4. Transmissive mode of operation.

It is important to note that the main thrust of this effort is in the area of T.V. type displays.

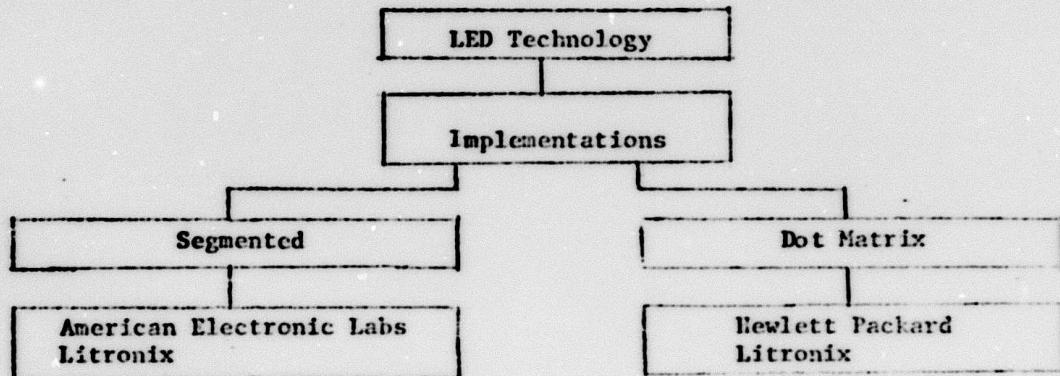
Thin-Film Semiconductor Techniques/L.C. Electro Optical Elements

Westinghouse--The Westinghouse Display Effort is being continued and has yielded an x-y addressable array of thin film transistors with intrinsic memory.

LED DISPLAY TECHNOLOGY

LED display technology is not in general use in large capacity displays due to the high power consumption of the LED display element. However for portable equipment requiring a modest number of characters as in the Micro-Terminal, the LED on balance competes rather favorably with presently available display technologies.

The accompanying chart depicts in very elemental fashion the findings of the survey in the Fall 1974.



The AEL device was selected for use in the engineering model for the following reasons:

1. The Micro-Terminal display requirement could be satisfied by modification of an AEL standard product
2. The display was acceptable in terms of power consumption, size, weight, form factor
3. The unit could be delivered within the required time frame and cost constraint.

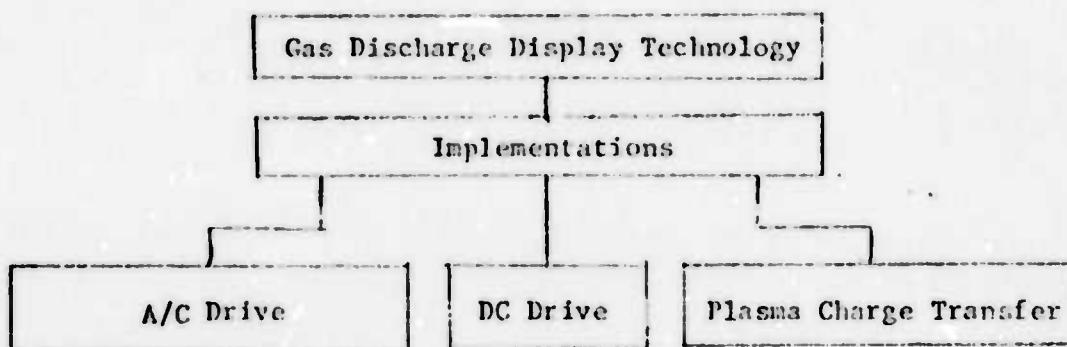
CURRENT STATUS OF LED DISPLAY DEVELOPMENT

No truly significant advances have been made in this area. HP will begin delivery of a 4 character stackable module on a sample basis within the next few months. Power consumption is estimated to be approximately 100 mw per character.

GAS DISCHARGE DISPLAY TECHNOLOGY

The primary characteristics of Gas Discharge Displays available in the Fall 1974 are summarized in Appendix B. The development effort in most companies was directed toward a standard product line exhibiting fixed formats and capacities. Companies expressed little or no interest in doing custom work.

The chart below depicts the basic implementations employed in flat panel alphanumeric Gas Discharge displays.



Gas Discharge Displays were eliminated for use in the engineering model on the basis of power consumption, character density (size), weight, and availability.

Current Status of Development

Recent work by National Electronics and Owens Illinois is directly applicable to the display requirements of the Micro-Terminal.

National has responded to a procurement enquiry. The information submitted was favorable in terms of power consumption of the display element but inadequate to determine power consumption at the system level. Additional information has been requested.

The recent work of Owens-Illinois will be described in a paper being presented at the U/M-SID Seminar at College Park, Maryland on Thursday, April 24, 1975. The title of the paper, "A Shift-Logic Plasma Display/Memory Unit." This technique will be evaluated against the specific display requirements of the Micro-Terminal.

Electroluminescent Display Technology

The work of interest in the above technology is being conducted primarily at the Westinghouse Research Laboratories. Westinghouse has been contacted and their effort is being monitored.

Their most recent effort combines electroluminescent phosphorus with thin film CdSe field effect transistors. The thin film transistor is a floating gate structure with intrinsic memory. The transistor provides both the selection and memory function yielding a simpler display from an operational viewpoint. This implementation can also be realized with liquid crystal electro optical elements.

The above implementation though attractive presents some tough process problems. The time frame for realization of this type of display is difficult to predict.

Cathode Ray Tube Technology

Cathode Ray Tube displays are clearly the best understood of the available display technologies in terms of capability as well as limitations. CRT displays exhibit a good figure of merit when viewed with regard to power consumption per character (estimated at 25-50 mw in a well designed system). Form factor is the key drawback for use in the Micro-Terminal.

Monitoring of this technology is being continued.

TELEVISION		LIQUID CRYSTAL, TYPE: DYNAMIC SCATTERING, MODE: REFLECTIVE		DELIVERY ≤ 4 MONTHS		DELIVERY ≤ 12 MONTHS	
AVAILABILITY	COMPANY	ITER	PRINCETON MAT. SCIENCE	HUGHES	HUGHES	PRINCETON MAT. SCIENCE	
GENERAL CHARACTERISTICS:							
Display Organization							
Character Capacity	480	80	40	280	80	2x40	
Display Format - Lines x Char/Line	10x48	2x40	1x40	5x7 dot matrix	5x7 dot matrix	5x7 dot matrix	
Character Format	14 seg. starburst	14 seg. starburst	5x7 dot matrix				
Character Size - "xH (in)	0.20x0.31	0.1x0.2	uns.	uns.	uns.	uns.	
Character Linear Density - (H,V)/in	4.0,2.0	3.0,2.0	8.1	10.7	10.4		
Optical Data							
Contrast Ratio - Typical							
Color (Char)	20:1	20:1	uns.	uns.	uns.	uns.	
Brightness	Light Gold	Black	Black	Black	Black	Black	
Viewing Angle	N.A.	N.A.	N.A.	N.A.	N.A.	N.A.	
45° Cone	45° Cone	uns.	uns.	uns.	uns.	uns.	
ELECTRICAL CHARACTERISTICS:							
Interface							
Logic Levels	Cmos-Type B	Cmos, τ_L^2	Cmos, τ_L^2	Cmos, τ_L^2	Cmos, τ_L^2	Cmos, τ_L^2	
Power Requirements	240-MW	40 MW	20 MW	140 MW	80 MW	80 MW	
Voltage Levels	+15.	+ (2-8)	uns.	uns.	uns.	uns.	
*Milli-Watts/Char-Typical	≤ 0.5	≤ 0.5	≤ 0.5	≤ 0.5	≤ 0.5	≤ 0.5	
Drive Requirements-IC Compatible	yes	yes	yes	yes	yes	yes	
PHYSICAL CHARACTERISTICS:							
*Dimensions - LxHxT (in)	14x5x2	13x2x1	6x1x1	4x2x1	4x1x1	4x1x1	
*Weight (lbs)	3.4	≤ 0.75	≤ 0.5	≤ 0.5	≤ 0.5	≤ 0.5	
ENVIRONMENTAL:							
Operating Temperature (°C)	0-50	(-5)-(+65)	uns.	uns.	uns.	uns.	
Humidity	Mil. Send 202 Method 105	uns.	uns.	uns.	uns.	uns.	
Storage Temperature (°C) & Humidity	Mil. Send 202 Method 103	uns.	uns.	uns.	uns.	uns.	
PRIMARY FEATURES:							
Drive Electronics	Included	Cnn supply 3-Char, 1-Line Nil	Included	Included	Included	Can supply	
Modularity (H,V)	4-Char, 1-Line	3-Char, 1-Line Nil	uns.	uns.	uns.	uns.	
Potential for Further Development	Moderate	Moderate	Yes-Material Properties Improve: TSW, Threshold Dev. Material with Memory Good	Yes-Improve Process & Materials	Yes-Material Properties Improve: TSW, Threshold Dev. Material with Memory Good	Yes-Material Properties Improve: Process & Materials	
Potential for Reduced Cost	Moderate	Moderate	Requirement for exacting material characteristics	Requirement for exacting material characteristics	Complex batch process yield must be consistent with cost constraint	Complex batch process yield must be consistent with cost constraint	
COMMENTS:	Interconnections: Drive Electronic to character segments	Interconnections: Drive Electronics to character segments	Good	Good	Requirement for exacting material characteristics	Requirement for exacting material characteristics	
Primary Problem Area							

NOTES: *Indicated values were either computed or conservatively estimated
on the basis of company furnished data.

Companies other than those appearing in the table were contacted.

The companies shown in the table appeared to be in the best

CODE:

uns. = unspecified

N.A. = not applicable

TECHNOLOGY - Gas Discharge

COMPANY	BURROUGHS	BURROUGHS	OMERS-ILL	OMERS-ILL	NIPPON ELECTRONICS	IEE	NATIONAL ELECTRONICS	DISCRIMIN
GENERAL CHARACTERISTICS:								
Display Organization	256	36	90/33	212/60	256	256	Custom	
Char Capacity	8 x 32	1 x 36	336	4335	8 x 32	8 x 32	5 x 7	
Display Format	5 x 7	5 x 7	5 x 7	5 x 7	5 x 7	5 x 7	5 x 7	
Char Format			0.135 x 0.193	0.077 x 0.112	0.180 x 0.267	0.213 x 0.3	0.130 x 0.190	
Char Dlm	0.7 x 0.28	0.14 x 0.28	6 x 3.3	10. 6	4. 2.6		6.4. 3.3	
Char Lineat Density(Kt/V)	2.6. 2.5	6. 2.5						
Modularity								
Optical Data								
Contrast Ratio	20:1	una	20:1	20:1	30:1	una	50:1	
Viewing Angle	113°	una	160° max	150° max	120°		150°	
Color	Neon-Orange	Neon-Orange	Neon-Orange	Neon-Orange	Neon-Orange			
Brightness (Nominal) (Fc-L)	25	Per Dot = 40						
ELECTRICAL CHARACTERISTICS:								
Interface Logic Levels	T ₂ L	T ₂ L	T ₂ L	T ₂ L				
Power Requirements (Watts)	31.6	9.9	104	287.5	42.5			
Voltage Levels	5. 30. -12. -250	5. -12. -250	115 AC + Power Pack	+12. 45. -12. -5. +2455	200. 160. +45. +22			
Current (amp)	2.5. 0.040. 0.26. 0.05	0.16. 0.050. 0.030	0.9	2.5	0.15. 2.0. 0.1. 0.2			
Multi-Starts/Char	123.5	24.0	297	72	71(7)			
I.C. Compatible	yes	yes	yes	yes				
PHYSICAL CHARACTERISTICS:								
Dimensions (LxWxT)	11.8 x 6.5 x 3.75	8.5 x 2.25 x 1.34	11.8 x 5.44 x 4.12	16.5 x 15.5 x 7.12	14 x 7 x 3	11.7 x 5.4 x 4.3	(2 x 80) 4 x 40	
Weight (lbs.)	una		Display: 7. T:Pack=5	Display: 33. P:Pack=17			12 x 2.0 x 2. 6 x 2.5 x 2.0	
ENVIRONMENTAL:								
Operating								
Temperature	0-50	0-50	0-50	0-50	0-50	(-5)-70	0-50	
Rel. Humidity	0-55%	0-55%	0-55%	0-55%	0-55%	uns	0-55%	
Storage								
Temperature	0-70	-4-85	(-62)-(+85)	0-70	0-70			
Rel. Humidity	0-55%	0-55%	0-55%	0-55%	0-55%	uns	0-55%	
PRIMARY FEATURES:								
Drive Electronics								
Modularity								
Potential for Further Dev.								
Life								
POTENTIAL FOR REDUCED COST:								
Comments:								

NOTE: For all sources except IEE - The dimensions for width, height, and thickness
are for the display and its housing since all electronics are included.

PORTABLE INTELLIGENT TERMINAL COMMAND SUMMARY

PROM
Bootstrap T --Bootstrap up from cassette tape.
 Ø --Run as an on-line terminal and bootstrap up from communications
 interface if escape character is received.
 X --Execute program bootstrapped in or left in memory from previous run.

Editing
Monitor Ø --Open tape file (displays name of file).
 C --Close file (tape or memory file).
 E --Edit file (go to editor level). If no file open, creates null
 memory file.
 T --Transfer files over communications link (Go to on-line level.)
 P --Purge tape file.
 R (record #) --Retrieve tape record (page of tape file).
 S (record #) --Store memory file on tape. (Updates old or creates new
 page of tape file.)

Editor †C--Go back to Editing Monitor.
 †A--Delete previous character.
 †B--Delete next character.
 †U--Delete back to beginning of line.
 †V--Delete forward to end of line.
 UP--Scroll cursor up, towards beginning of file (display scrolled down).
 DØWN--Scroll cursor down, towards end of file (display scrolled up).
 (Note: UP and DØWN scroll a single display line, not necessarily
 a full text line, when button first pressed. If button held down
 longer than a short delay, begins scrolling at a fixed readable
 rate. †UP and †DØWN provide rapid scrolling capability.)
 Shift UP--Scroll cursor left on current display line.
 Shift DØWN--Scroll cursor right on current display line.
 (Note: Shift UP and Shift DØWN move cursor a single character
 position when first depressed. If button held down longer than a
 short delay, begins continuous scrolling to beginning or end of the
 display line.)

ON-LINE
Terminal

Sends keys out to communications interface and displays incoming information. The details for the file transfer have not been worked out yet. An escape key from the keyboard will allow for local commands (such as half or full duplex, jumping back to Editing Monitor, or possibly beginning file transfer in or out). Also, an escape character being received may delimit file transfer operations.

Two alternatives are currently being considered for the file transfer:

1. An escape key followed by local commands may initiate the transfer which will then be controlled by the local process.
2. Commands sent to the remote process initiate the transfer which is then controlled by the remote process (communicating with the local process via an escape code).

Note: The single button commands may be changed or replaced by multiple key mnemonics.

COMPUTER SYSTEMS
LABORATORY--UCSB
23 April 1975
Microterminal Report #8
Paul E. Wells

PROTOTYPE POWER SOURCES-
SPECIFICATIONS AND DIAGRAMS

1.0 SYSTEM POWER ALLOCATION PROTOTYPE UNITS

SUBSYSTEM	POWER-(WATTS)	LEVEL-(V)	CURRENT-(AMPS)	TOTAL SUBSYSTEM POWER-(WATTS)
<u>Terminal Controller</u>				13.895
Micro-Processor	1.000	+5	0.200	
Memory	1.000	+5	0.200	
Control & I/O	0.500	+5	0.100	
	1.200	+12	0.100	
Display	9.000	+5	1.800	
	0.600	-12	0.050	
Keyboard	0.175	+5	0.035	
	0.420	-12	0.035	
<u>Tape Unit</u>				4.000
Logic	1.000	+5	0.200	
Servo	1.750	+5	0.350	
R/W Circuits	1.250	-5	0.250	
<u>Acoustic Coupler</u>				4.900
	2.500	+5	0.500	
	1.200	+12	0.100	
	1.200	-12	0.100	

Note: The allocated power levels are not a true measure of equipment average power consumption.

2.0 POWER-PACK IMPLEMENTATION

The implementation of the Power-Pack is based on components which are available within a practical time frame. The development effort of commercial manufacturers in the battery area, and NASA in the converter area, suggests the possibility of substantial improvement in future power-pack designs.

2.1 DESIGN CONSIDERATIONS

The following Power-Pack implementation options were considered:

1. Single battery in combination with a series regulator and a multiple output DC/DC converter supplying all subsystems.
2. Separate power-packs, implementation as stated above, for each major subsystem.

Option 2 was selected for the following reasons:

1. DC/DC converter required for option 1 was not available.
2. Relaxed packaging constraints.
3. Allows independent testing of each major subsystem

Block diagrams are shown below. The indicated current is a capability level.

The series regulator design is quite straightforward and the efficiency should be approximately 80%. The high conversion efficiency is the result of the small difference between V_{in} and V_{out} over the usable portion of the battery discharge curve. (See Typical Discharge Characteristic of Ag-Zn Cell.) The converters are available as off-the-shelf items. The reflected ripple from the converters is of some concern, but should present no problem if the units conform to published specifications.

Considerable engineering effort has been expended in the space program toward the development of high efficiency power converters. In the 3 watt range the pseudo-saturating core technique is quite effective. Efficiencies of approximately 80% were achieved over the power range of 0.7 to 3.0 watts. Since either option requires a converter, some engineering effort in the converter area is probably warranted in order to optimize the design of the power-pack (future work).

A minimal effort was expended in the area of switching type regulators. Switching type regulators are more complex than the conventional series regulator and offer no significant advantages in low power systems where $V_{in}-V_{out}$ is $\leq 1.5V$.

2.2 BATTERIES

The batteries are manufactured by the Yardney Electric Corporation. Each battery is comprised of four rechargeable high energy density silver-zinc cells.

Terminal Controller

Cell Capacity-(A-Hr @ 10 Hr Discharge Rate)	:	20
Battery Capacity-(Avg. W-Hr)	:	121
Battery Size - (in.)	:	4.94x9.24x0.80
Battery Weight - (lbs.)	:	2.5
Battery Discharge Curve	:	See Typical Discharge Characteristics*

Tape Unit

Cell Capacity -(A-Hr @ 10 Hr Discharge Rate)	:	6
Battery Capacity - (Avg. W-Hr.)	:	36.6
Battery Size - (in.)	:	1.72x3.36x0.59
Battery Weight - (oz.)	:	14.4

Acoustic Coupler - same as Tape Unit

2.3 DC/DC CONVERTERS

The converters are off-the-shelf commercial grade units. The units incorporate current limiting. Overvoltage protection must be added to each voltage level.

Terminal Controller

Semiconductor Circuits, Inc. Model No. 30C5-12D125

Capacity - (Watts)	:	3
Weight - (oz.)	:	3
Size - (in.)	:	1.5 x 2.0 x 0.4

Tape Unit

Technetics Model 1305-105

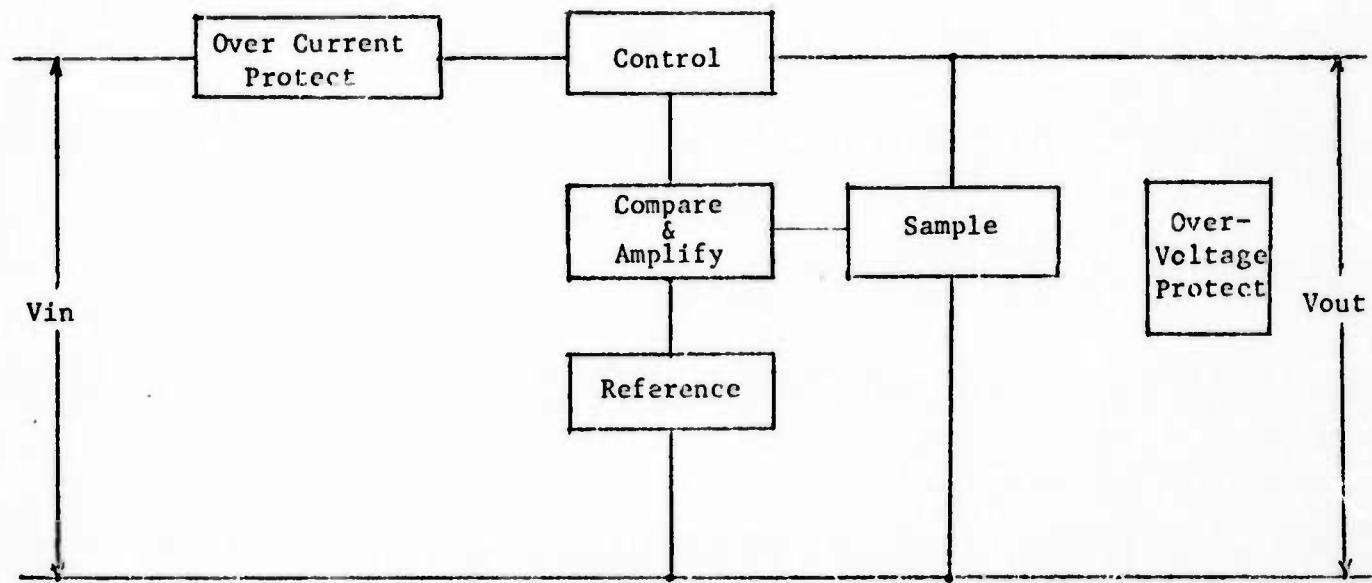
Capacity - (Watts)	:	3
Weight - (oz.)	:	6
Size - (in.)	:	2.125x2.35x0.812

Acoustic Coupler - same as Data Terminal

*See Report #5.

2.4 D-C VOLTAGE REGULATOR

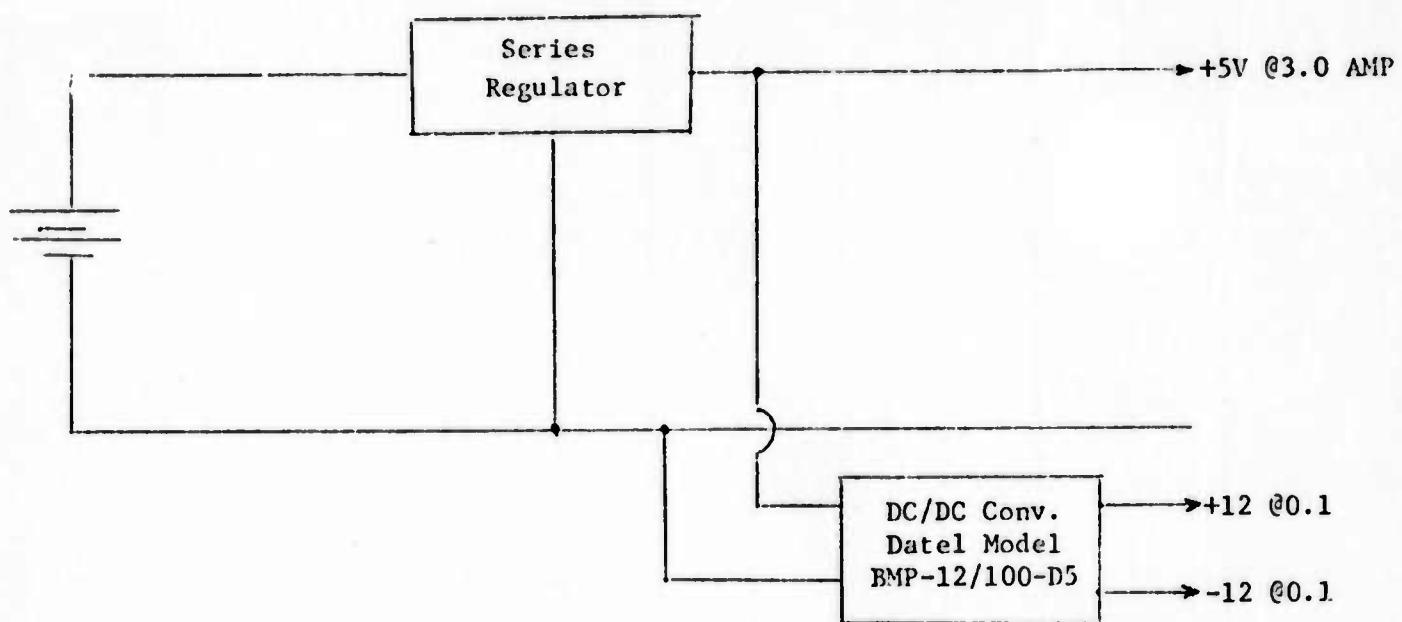
The D-C Voltage Regulator is a conventional Series Type regulator.
(See block diagram.)



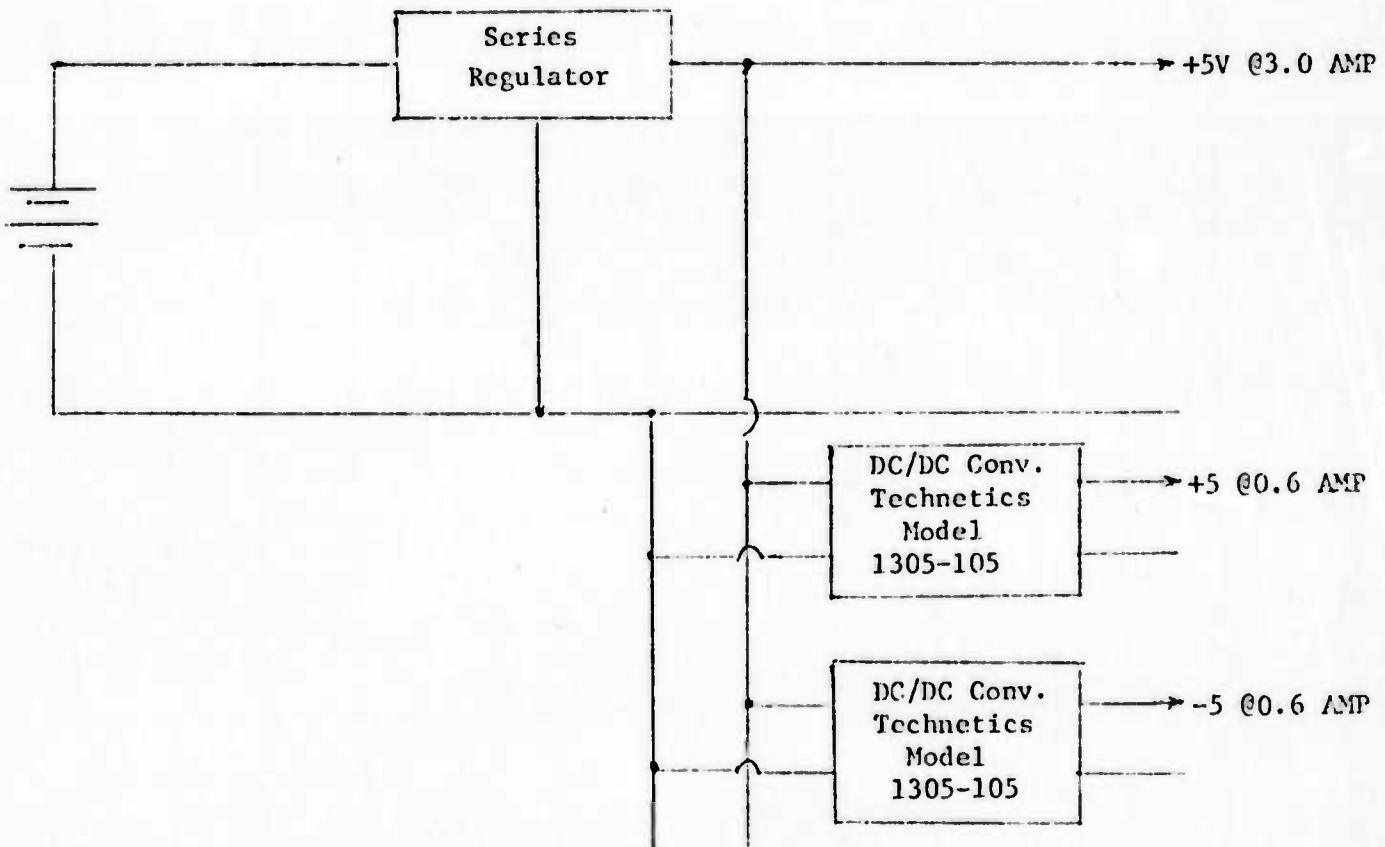
Block Diagram - Series Regulator

3.0 CROSS-REFERENCE TO ALTERNATE BATTERIES

The chart (next page) illustrates the differences in Watt-Hr. to Weight for various types of batteries. In the prototype we employ Yardney Silver-Zinc cells (Type LR15). As power requirements diminish, in later versions of the micro-terminal, we may be able to use nickel cadmium which is cheaper and trouble-free.



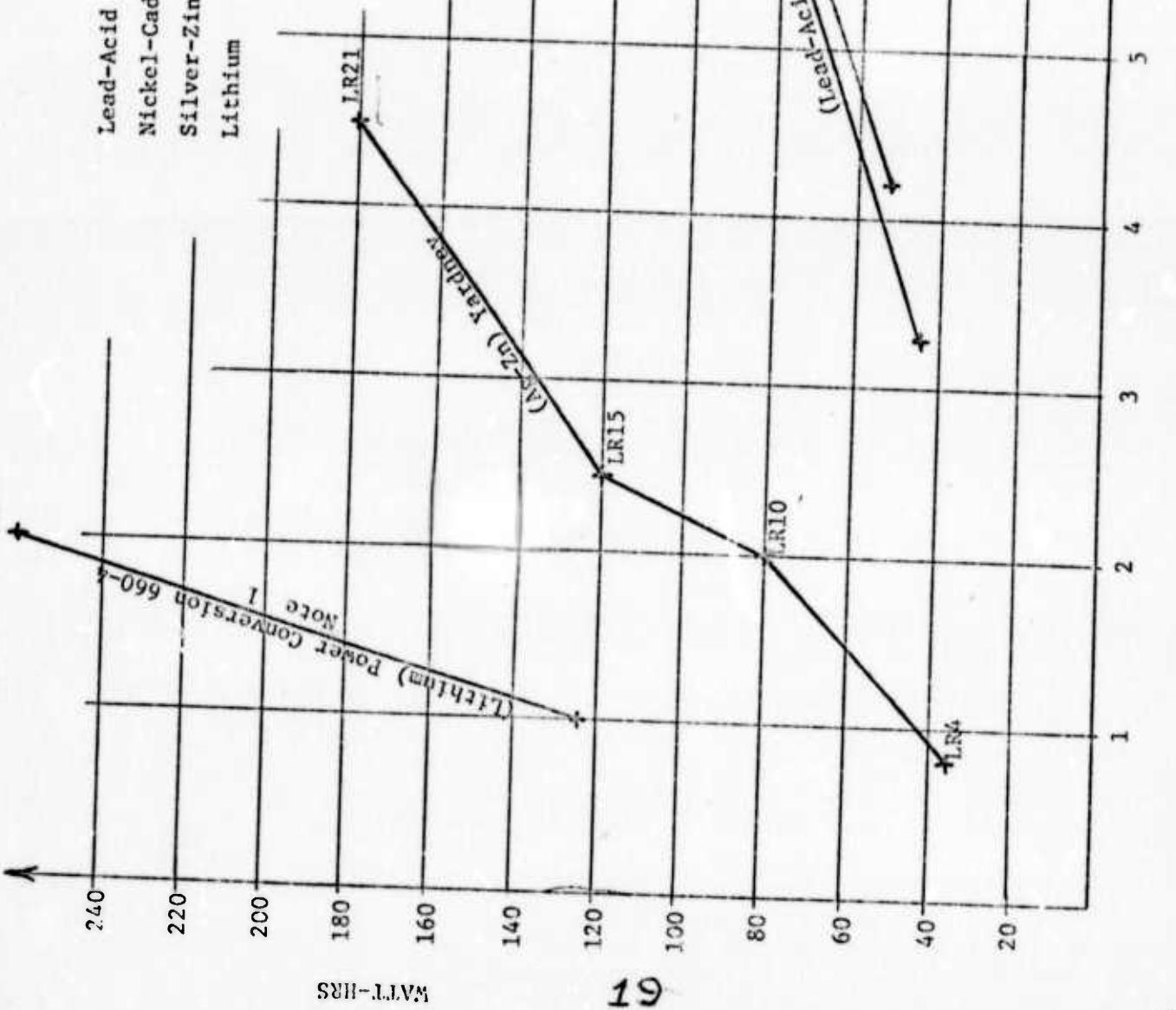
Power-Pack Configuration: 1 ea. Data Terminal & Acoustic Coupler



Power-Pack Configuration: Tape Unit

APPROXIMATE PERFORMANCE TABLE

	Energy W-Hr/Lb	Cell Voltage	Cell Vol In/Lb	Number Recharge Cycles
Lead-Acid	14	2.00	15	500
Nickel-Cadmium	15	1.32	15	1000
Silver-Zinc	53	1.60	15	80-100
Lithium	127	2.80	15	0



Note 1

electron delocalisation brought about by d_s - d_s overlap between tin and the transition metal. This reduces ΔE in equation (1) so that the paramagnetic contribution is larger and the total shielding is decreased. This effect is offset by heavy atoms so that shifts to high field are observed when a third-row transition element is

present, and intermediate results are obtained for the second row metals.

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